

FIG. 1

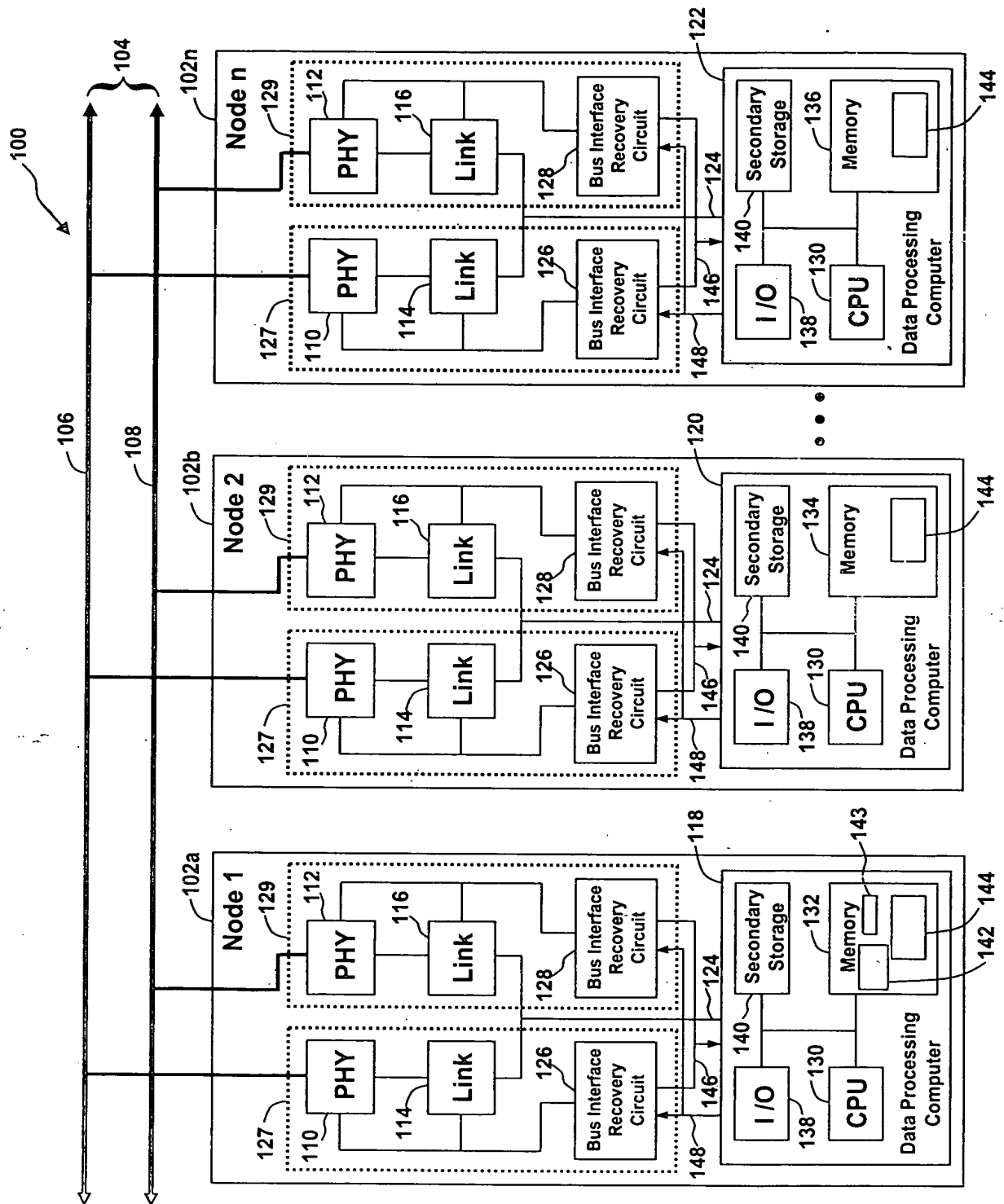


FIG. 2

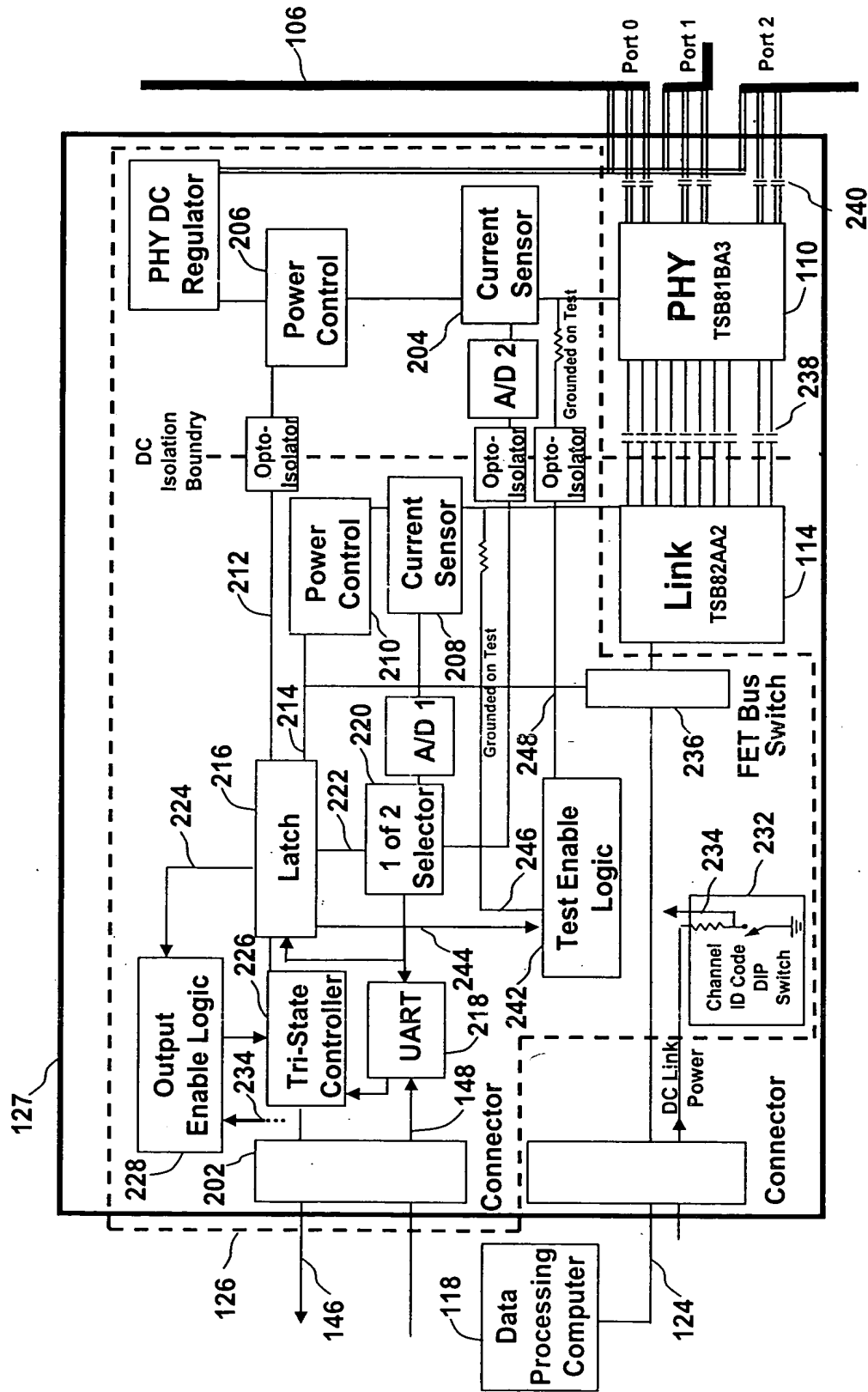


FIG. 3

| | | |
|-----|----------------|--|
| 300 | <u>Bit No.</u> | |
| | 1 | 1 = Power Down Command, Channel A Link |
| | 2 | 1 = Power Down Command, Channel A PHY |
| | 3 | 1 = Power Down Command, Channel B Link |
| | 4 | 1 = Power Down Command, Channel B PHY |
| | 5 | 1 = Activate Current Surge Test for the Enabled Outgoing UART Source |
| | 6 | 0 = Enable Channel A for Outgoing UART Data, 1 = Enable Channel B |
| | 7 | 0 = Enable Monitoring of Link Current for Outgoing UART Data, 1 = Enable PHY Current Monitoring |
| | 8 | Reserved - Set to 0 in Control Latch |

FIG. 4

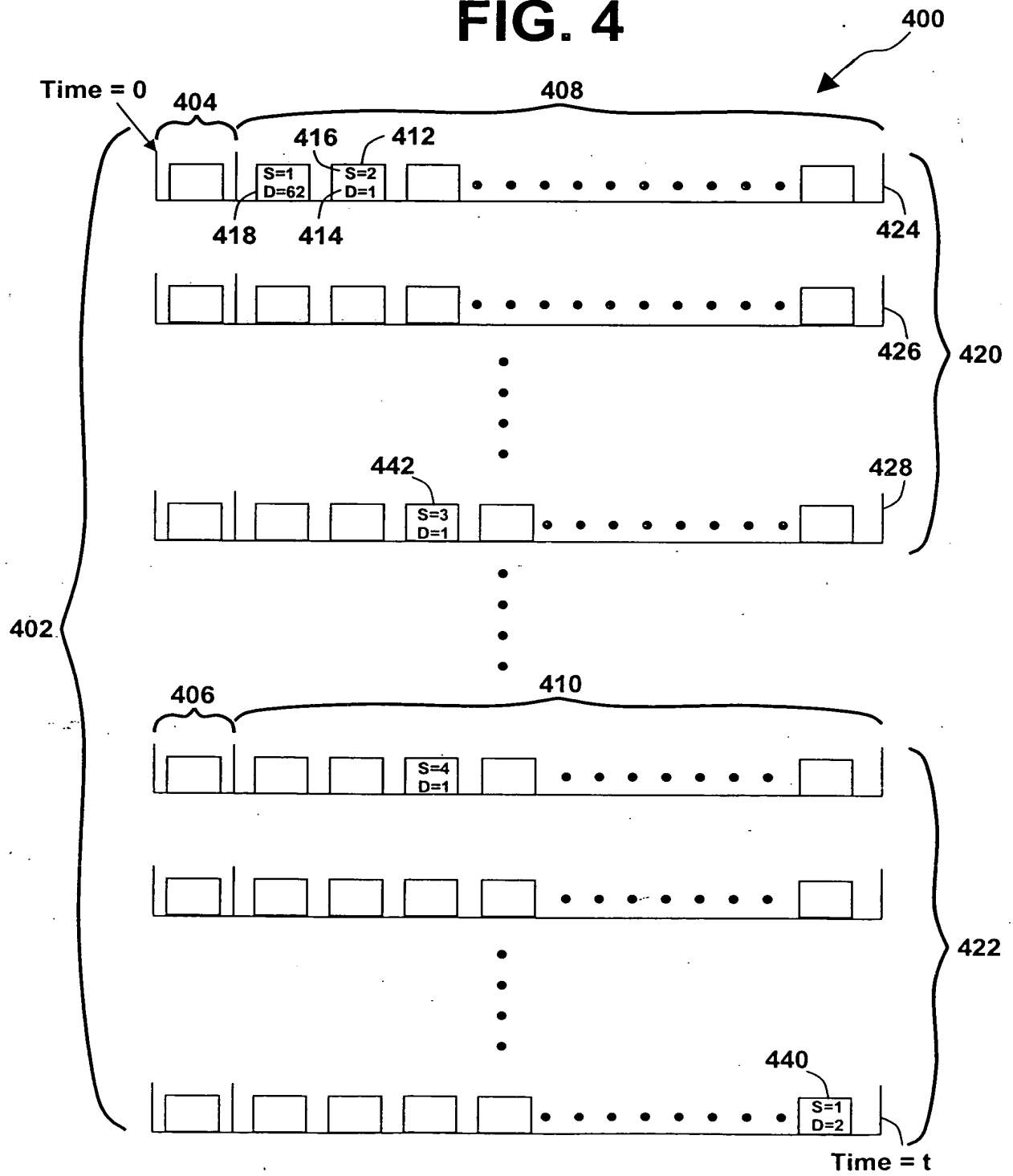


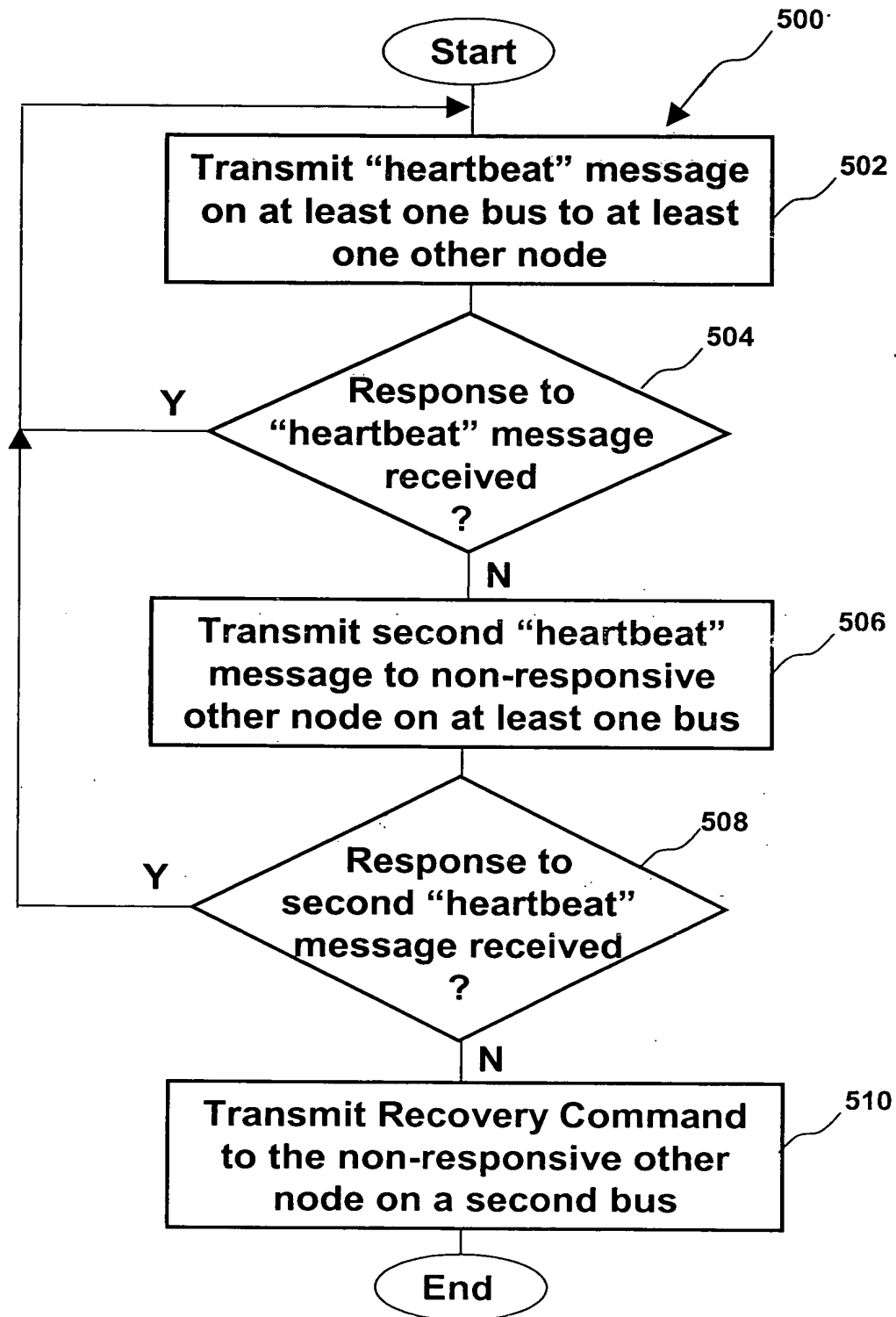
FIG. 5

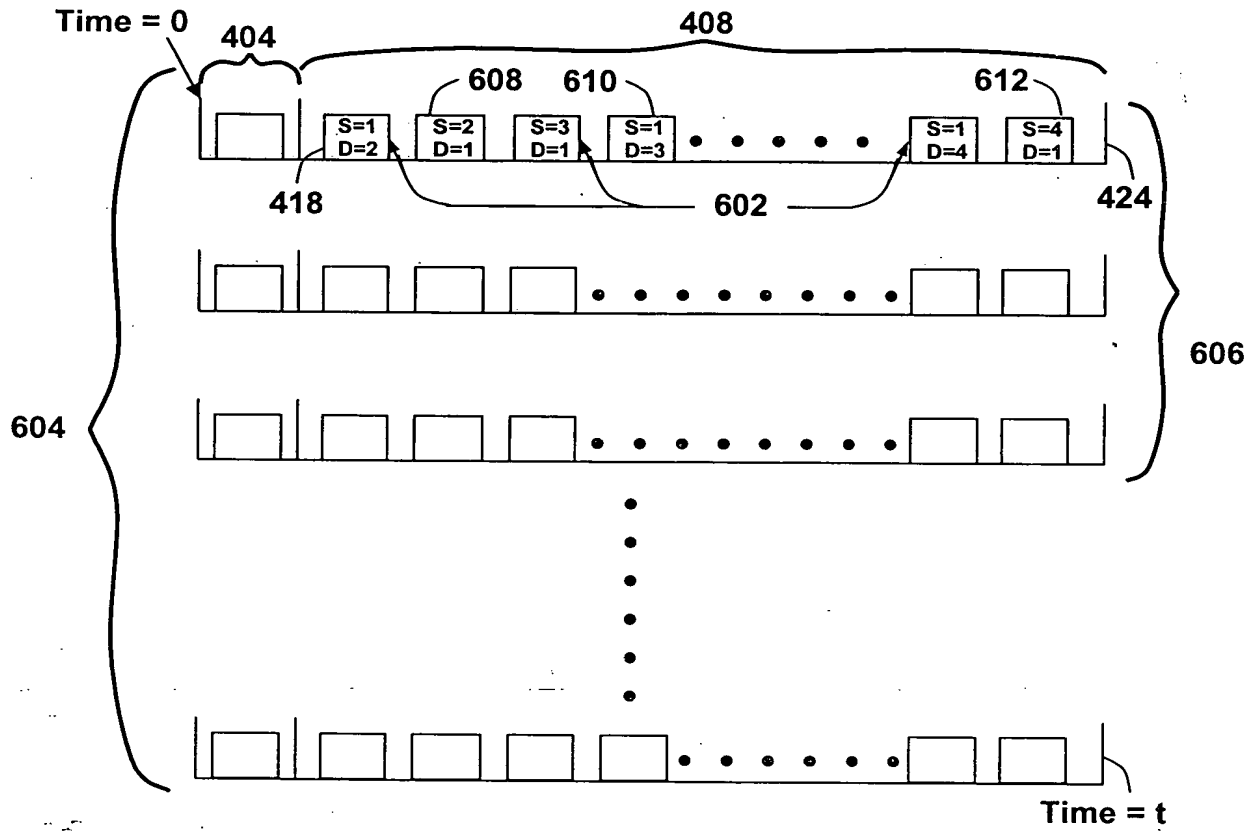
FIG. 6

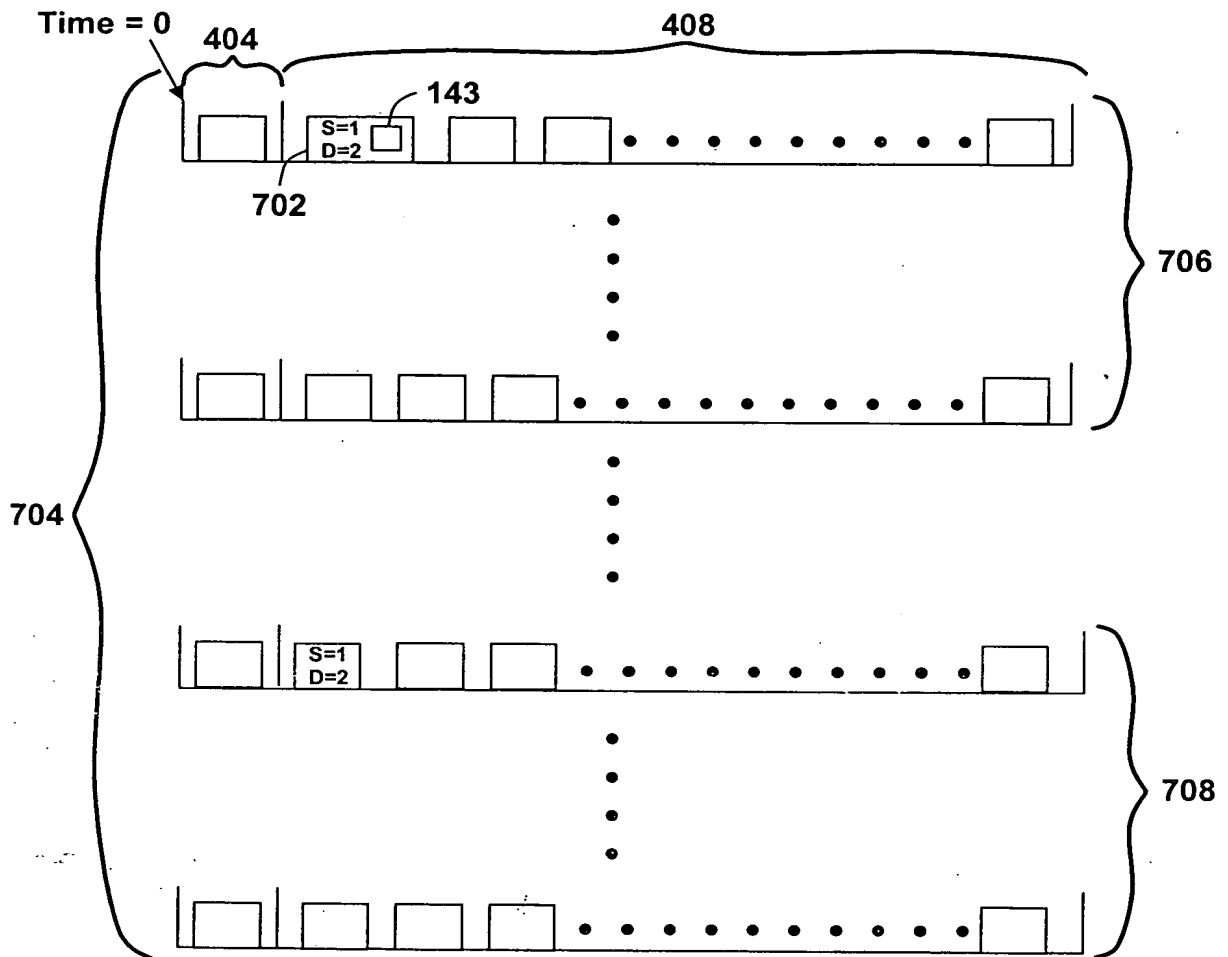
FIG. 7

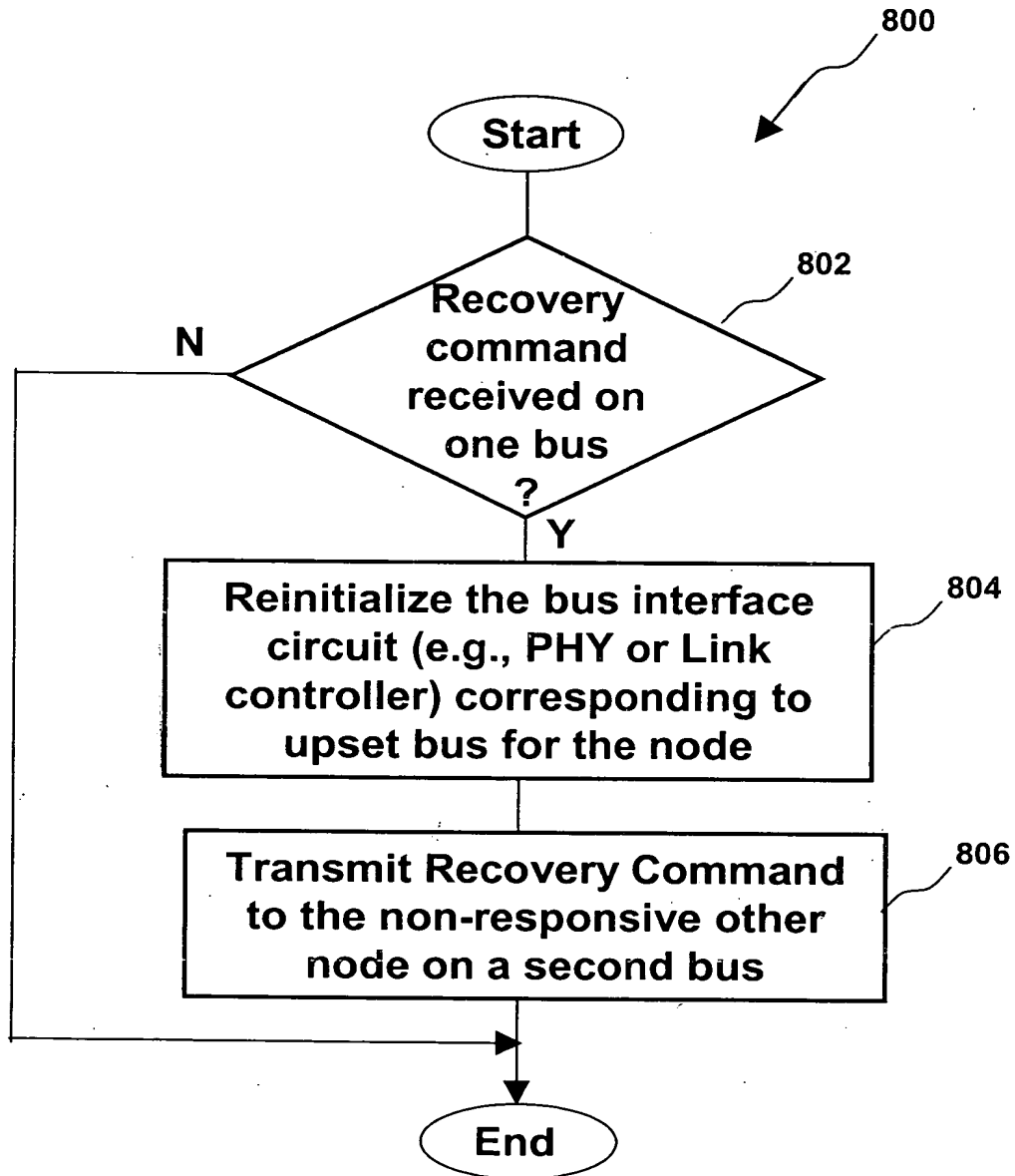
FIG. 8

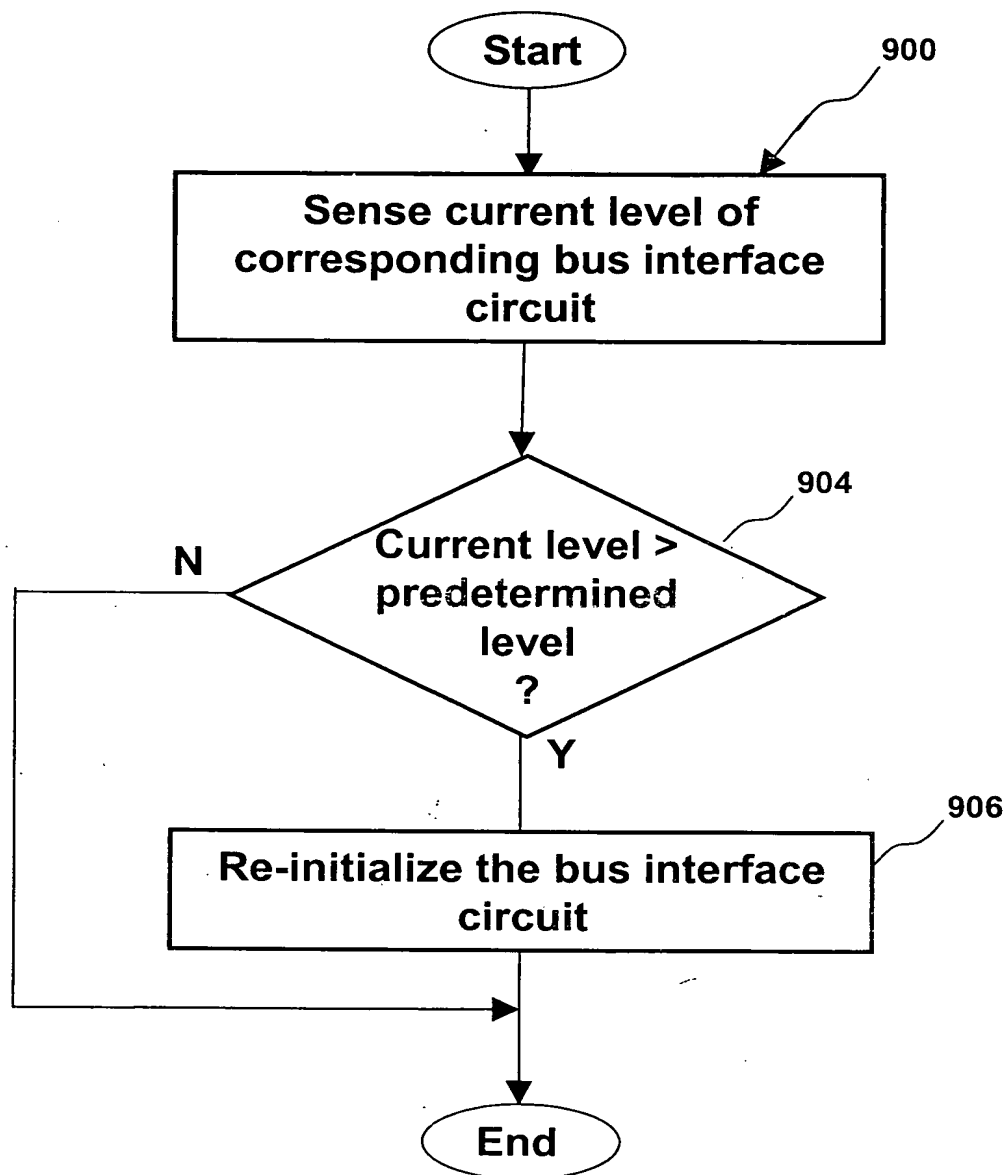
FIG. 9

FIG. 10

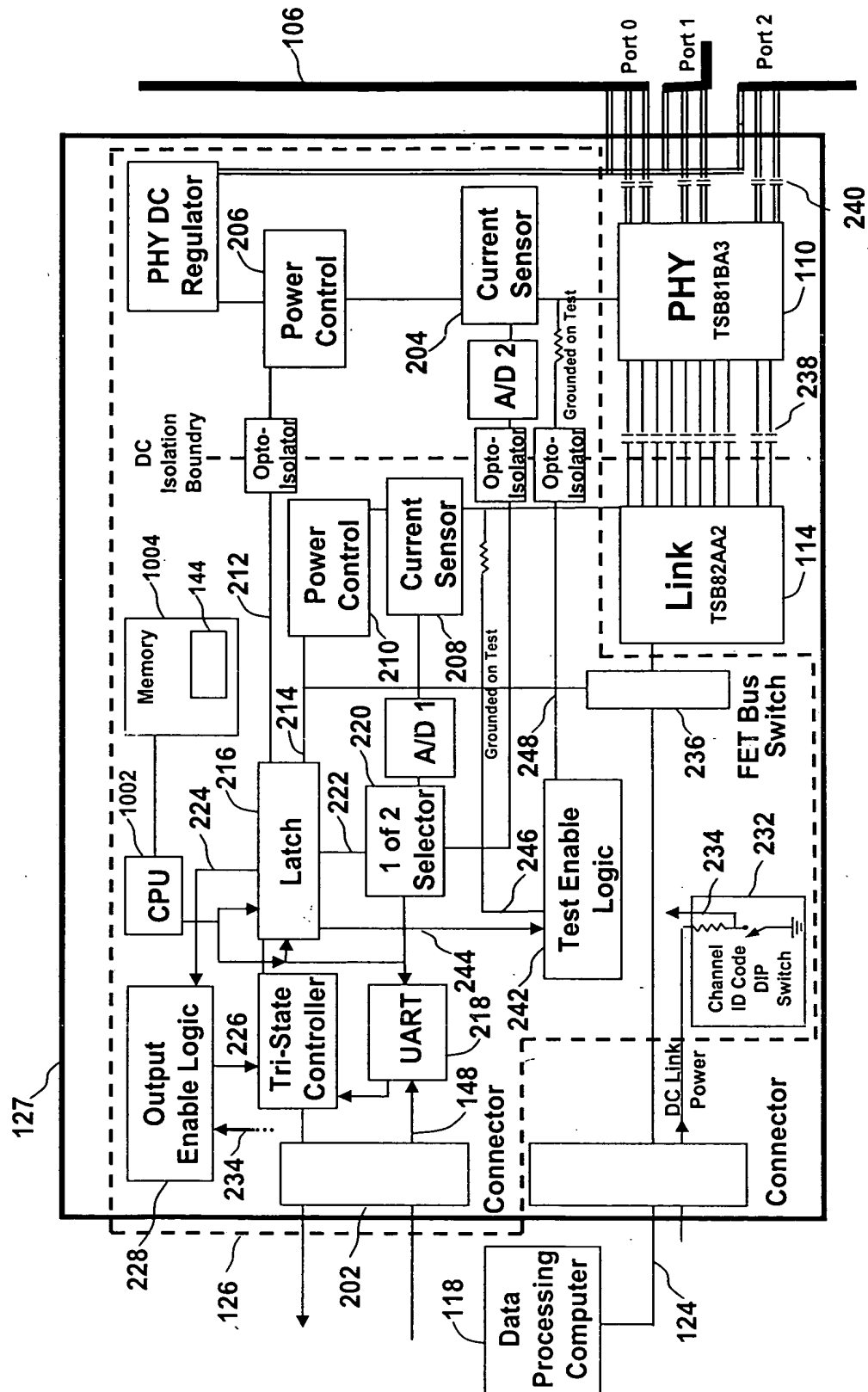


FIG. 11

